

Roland F. Jones. B.Sc. (ENG) Hons.

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Objective: An engineering development/production position within an expanding electronics company, where initiative, adaptability, excellent communication skills are an important part of the job function, and independence is encouraged. Stability in one geographical area is a consideration although travel is acceptable.

Experience:

- Over nine years working in the VLSI IC industry on new product introduction, manufacturing, and test.
- Worked with IC design to ensure full design testability.
- Defined and helped create device test solution, through hardware and software.
- Organized and performed device, package, fab, and stress characterization on the parts, along with extensive statistical analysis of results.
- Worked with failure analysis department to resolve issues highlighted by qualification and characterization.
- Interfaced with marketing and the applications group providing technical information internal to the company and also to outside customers.
- Interfaced with production control to obtain required production resources.
- Provided simple presentations and training courses for the test floor on the new product and the test system being used.
- Stabilized long term production prior to handing over day to day issues to others.
- Ultimate responsibility for all of the above functions as the project leader.

Achievements:

- Fast reaction and good management of several sudden changes in project status (cancellations, reinstatements, sudden large orders and pull-ins).
- Very fast device production ramps.
- High degree of initiative, motivation, and personal commitment to the project.
- Highly effective in organizing and managing resources within the project team.
- Successfully set up off shore test at a previously unused subcontractor in Korea within very tight time constraints.
- Quickly picked up and completed small jobs for other projects as time allowed.

Skill Summary:

Technical

- Firm understanding of experimental statistics, both classical and resistant.
- Extensive experience of digital and mixed signal CMOS design techniques and fabrication processes.
- Experience in use of custom VLSI design and simulation tools.
- Extensive experience in the use of VLSI test systems.
- Very high level of computer literacy.

Personal

- Very high level of communication and people skills, very easy to get on with.
- Highly self motivated and autonomous, though readily seeks help when required.
- Quick to adapt to changes and new situations.
- Very dedicated, can apply a lot of time and energy to a project when required.
- Travels easily, does not suffer from jet lag and adapts well to new environments.
- High level of integrity.

Computing Experience:

Systems worked on Sinclair MK14, PDP-11, IRI P256 Vision Computer, BBC-B, Methius, Sun Workstation, VAX-11, Tandon PAC 286, Apollo Workstation, NCC 486 PC-compatible.

Operating systems used VAX VMS, Domain, MSDOS, UNIX, MS Windows, X Windows, Windows 95.

Packages used Latex, Lotus 123, POPLOG, SAS, TDS2, Word Perfect, Word Star, Norton Desktop, MS Word, MS Excel, MS Project, Eudora, Netscape/HTML.

VLSI testers used Inmos T³E, Teradyne J941 and A580, Trillium Micro Master and Delta Master.

IC design packages used Spice, LSim, Hilo, ved, Opus.

Programing languages used Basic, C, Forth, Fortran 77, Lisp, Occam 2, Pascal, POP-11.

Assembly languages used 6502, 6809, SC/MP, Z80.

Career History:

Senior Development Product Engineer - Brooktree Corp., San Diego, California. - May '91 to Present.

IC Product Engineer - Inmos Ltd., Newport, UK. - May '87 to Apr. '91.

IC Production Engineer with design - Philips Components, Southampton, UK. - Sept. '86 to Apr. '87.

Education: Queen Mary College, University of London. - 1983 to 1986.
B.Sc. (ENG) Hons. - Electronic Engineering (1st class).
Elective Subjects - Artificial Intelligence, Control Theory, VLSI & Digital Circuit design.

Miscellaneous Personal Details: US residency status :- resident alien.

References: Available on request.